

**AMENDMENT TO THE SPECIFICATION**

Please amend the paragraph beginning on page 9, line 7 as follows:

--The stated aim can be achieved by a nonvolatile memory microcomputer chip including a microcomputer unit and a memory unit, the microcomputer unit including: a plurality of circuit blocks including a CPU, and the memory unit including: a nonvolatile memory operable to store therein test data used for testing when the testing is performed, and operation data used for an operation other than the testing when the operation is performed; a memory control unit operable to (a) acquire a plurality of pieces of test data from outside the nonvolatile memory microcomputer chip and store the plurality of pieces of test data in the nonvolatile memory, and then (b) control the nonvolatile memory to sequentially output a plurality of test signals which each show a piece of test data out of the plurality of pieces of test data; a drive unit operable to supply each of the plurality of test signals sequentially output from the nonvolatile memory, to any of the plurality of circuit blocks that is to be tested using a piece of test data shown by the test signal, to drive the circuit block; and an output unit operable to receive a test result signal from the driven circuit block, and output the test result signal to outside the nonvolatile memory microcomputer chip.--